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**REMARKS**


Supplemental to Applicant's Request for Reconsideration filed July 17, 2006, it is respectfully requested that the above-identified application be amended in accordance with those amendments set forth hereinabove. Specifically, claims 1, 21 and 26 have been amended in order to better define that which Applicant regards as the invention. As previously, claims 1-29 are presently pending in the instant application with claims 6-20 being withdrawn from further consideration by the Examiner as being directed to a non-elected invention.

It is respectfully submitted that the foregoing amendments are being submitted in order to make independent claims 1, 21 and 26 commensurate with the arguments set forth in Applicant's response filed July 17, 2006. Specifically, as is now clearly recited by Applicant's claimed invention, the test mode circuit outputs a test mode signal according to a predetermined voltage being supplied to a predetermined terminal and a plurality of address input terminals. As noted throughout Applicant's previous response, the Applicant's Admitted Prior Art as well as those references of record clearly fail to disclose or remotely suggest this feature. Accordingly, it is respectfully submitted that Applicant's claimed invention as set forth in each of independent claims 1, 21 and 26 as well as those claims which depend therefrom clearly distinguish over the prior art of record and are in proper condition for allowance.

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Again, should the Examiner believe a conference would be of benefit in expediting the prosecution of the instant application, he is hereby invited to telephone counsel to arrange such a conference.

Respectfully submitted,

  
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